

Washingtons Defro 20231

Transmitted Herewith for filing is the Patent Application of:

Inventor: CHIUN-CHI SHEN

For: A DELAY LOCKING HIGH SPEED CLOCK SYNCHRONIZATION METHOD AND CIRCUIT

Enclosed are:

|x| $\underline{7}$ sheets of drawing(s) - formal.

X An assignment of the invention to Etron Technology, Inc.

An associate power of attorney

the filing fee has been calculated as shown below:

	(Col. 1)	(Col. 2)	OTHER THAN A	SMALL ENTITY
FOR:	NO. FILED	NO. EXTRA	RATE	FEE
BASIC FEE		\searrow		\$ 690.
TOTAL CLAIMS	46 - 20=	26	x 18 =	\$ 468.
INDEP CLAIMS	5 -3=	2	x 78 =	\$ 156.
MULTIPLE DEPENDENT CLAIM PRESENTED + 260 =				
		ST	JB TOTAL	\$ 1,314.
		·AS	SSIGNMENT	\$40.
		TO	OTAL	\$ 1,354.

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X Any additional filing fees required under 37 CFR §1.16.

 $|\mathbf{x}|$ Any patent application processing fees under 37 CFR §1.17.

Respectfully symmitted,

STEPLEN B. ACKERMAN, REG. NO. 37,761